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INFORMATION DISCLOSURE

Joseph A. Bennett, et al. **First Named Inventor** Not Yet Assigned Group Art Unit **Examiner Name** Not Yet Assigned Attorney Docket Number 42390P10976

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Examiner Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²		
ily		INTEL, Intel® 440GX AGPset: 82443GX Host Bridge/Controller, Datasheet; June 1998, pages 126.			
W		PCI LEGAL BUS, PCI-X Addendum to the PCI Local Bus Specification, Revision 1.0a, July 24, 2000, pages 240.			
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